3	1	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4
		СС	ond		0	1	I	Р	U	В	W	L		R	n I			R	d							off	set	
																	<pre>source / destination register base register load / store bit 0 = store to memory 1 = load from memory write-back bit 0 = no write-back 1 = write address into base byte / word bit 0 = transfer word quantity 1 = transfer byte quantity 1 = transfer byte quantity up / down bit 0 = down, subtract offset from base 1 = up, addition offset to base</pre>											
							25														11	10	9	8	7	6	5	4
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